

Paola Faraboschi

Paolo Faraboschi is a Vice President and HPE Fellow and directs the AI Research Lab at Hewlett Packard Labs. Paolo has been at HP/HPE for longer than he would like to admit, and worked on a broad range of technologies, from embedded printer processors to exascale supercomputers. He previously led exascale computing research (2017-2020), and the hardware architecture of “The Machine” project (2014-2016), pioneered low-energy servers with HP’s project Moonshot (2010-2014), drove scalable system-level simulation research (2004-2009), and was the principal architect of a family of embedded VLIW cores (1994-2003), widely used in video SoCs and HP’s printers. Paolo is an IEEE Fellow (2014) for “contributions to embedded processor architecture and system-on-chip technology”, author of 70+ granted patents, 100+ publications, and the book “Embedded Computing: a VLIW approach”. He received a Ph.D. in microelectronics from the University of Genoa, Italy.